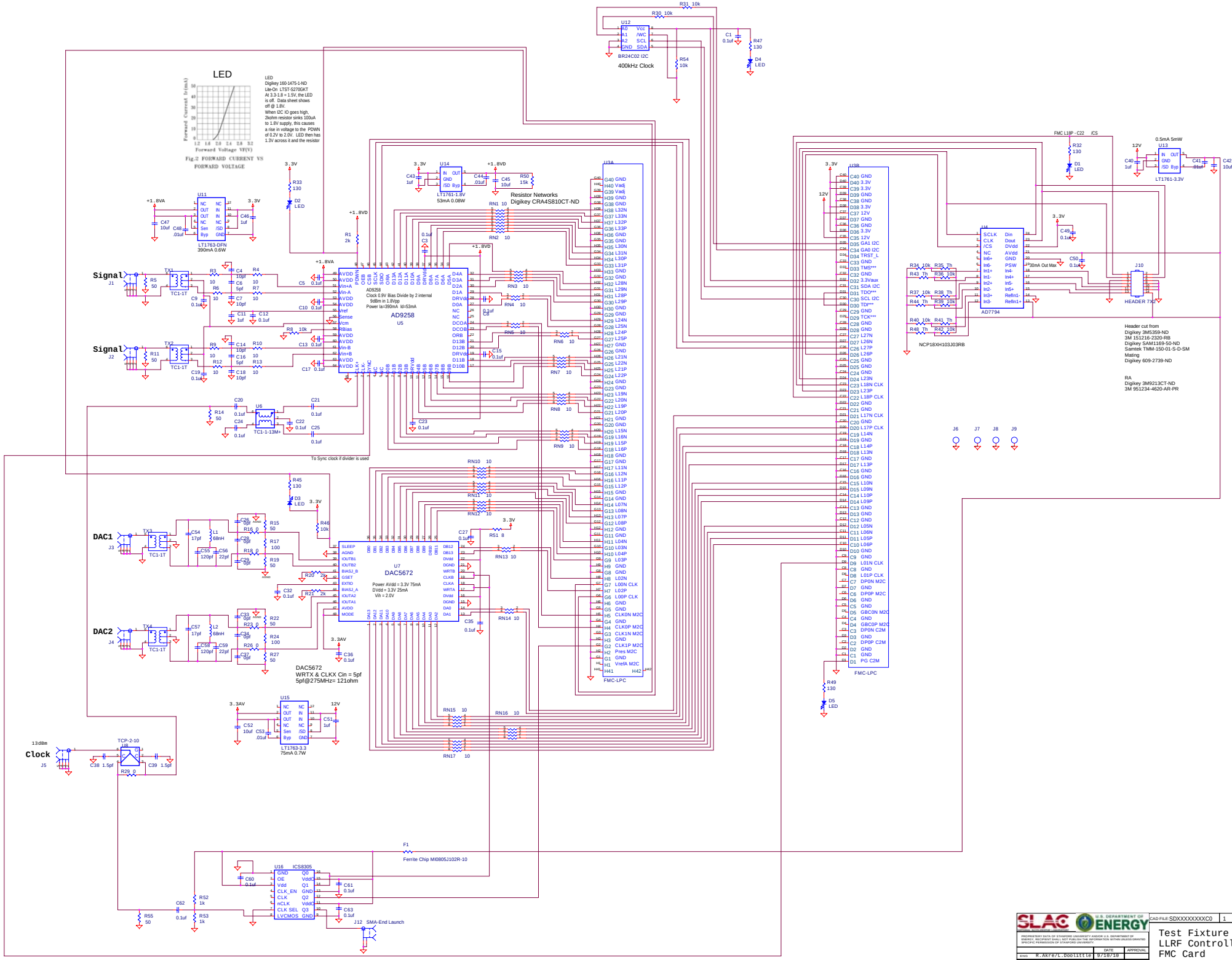
LED
Forward Current Ie(mA)
Forward Voltage VF(V)
Fig.2 FORWARD CURRENT VS FORWARD VOLTAGE
LED
Digikey 160-1475-1-ND
Lite-On LTST-S270GKT
At 3.3-1.8 = 1.5V, the LED is off. Data sheet shows off @ 1.8V.
When I2C IO goes high, 2kohm resistor sinks 100uA to 1.8V supply, this causes a rise in voltage to the PDWN of 0.2V to 2.0V. LED then has 1.3V across it and the resistor
U12
BR24C02 I2C
400kHz Clock
R30 10k
R31 10k
R54 10k
C1 0.1uf
R47 130
D4 LED
U11
LT1763-DFN
390mA 0.6W
U14
LT1761-1.8V
53mA 0.08W
U13
LT1761-3.3V
0.5mA 5mW
Resistor Networks
Digikey CRA4S810CT-ND
Signal
J1
Signal
J2
TX1
TX2
TC1-1T
AD9258
U5
Clock 0.9V Bias Divide by 2 internal
9dBm in 1.8Vpp
Power Ia=390mA Id=53mA
U6
TC1-1-13M+
To Sync clock if divider is used
DAC1
J3
DAC2
J4
TX3
TX4
U7
DAC5672
Power AVdd = 3.3V 75mA
DVdd = 3.3V 25mA
Vih = 2.0V
DAC5672
WRTX & CLKX Cin = 5pf
5pf@275MHz= 121ohm
U15
LT1763-3.3
75mA 0.7W
13dBm
Clock
J5
TCP-2-10
U16
ICS8305
J12 SMA-End Launch
F1
Ferrite Chip MI0805J102R-10
U3A
FMC-LPC
U3B
FMC-LPC
U4
AD7794
NCP18XH103J03RB
J10
HEADER 7X2
Header cut from
Digikey 3M5359-ND
3M 151216-2320-RB
Digikey SAM1169-50-ND
Samtek TMM-150-01-S-D-SM
Mating
Digikey 609-2739-ND
RA
Digikey 3M9213CT-ND
3M 951234-4620-AR-PR
J6
J7
J8
J9
D1 LED
D2 LED
D3 LED
D5 LED
R32 130
R33 130
R45 130
R49 130
FMC L18P - C22 /CS
SLAC
U.S. DEPARTMENT OF ENERGY
CAD FILE SDXXXXXXXXXC0
1 of 1
Test Fixture
LLRF Controller
FMC Card
DRAWN R.Akre/L.Doolittle 9/10/10
DATE
APPROVAL
CHECK
LAST MOD Monday, October 04, 2010
SD-XXX-XXX-XX
R
E
Spires: LLRF FMC ADC DAC Control