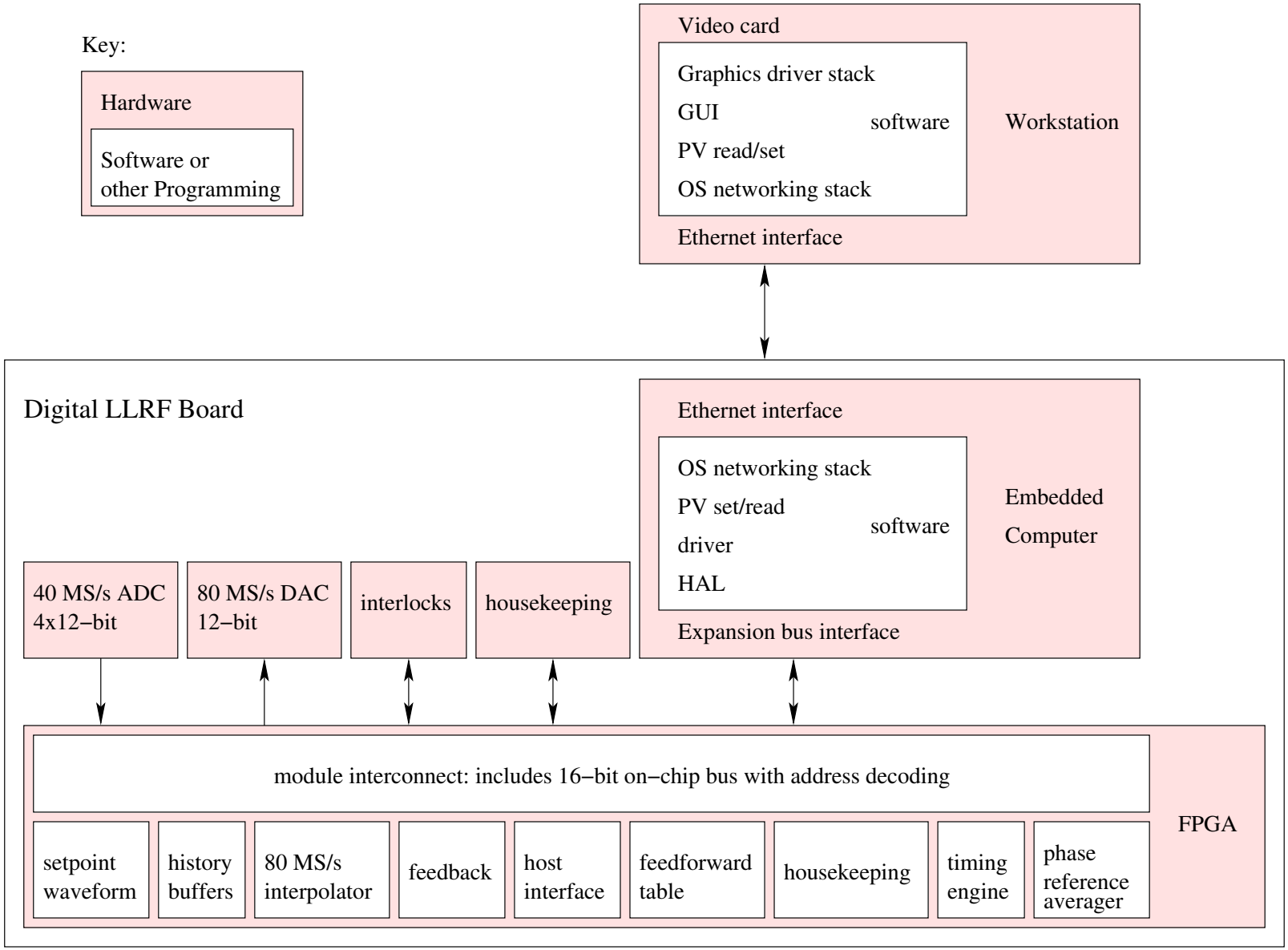
Key:
Hardware
Software or
other Programming
Video card
Graphics driver stack
GUI
PV read/set
OS networking stack
software
Workstation
Ethernet interface
Digital LLRF Board
Ethernet interface
OS networking stack
PV set/read
driver
HAL
software
Embedded
Computer
Expansion bus interface
40 MS/s ADC
4x12−bit
80 MS/s DAC
12−bit
interlocks
housekeeping
module interconnect: includes 16−bit on−chip bus with address decoding
setpoint
waveform
history
buffers
80 MS/s
interpolator
feedback
host
interface
feedforward
table
housekeeping
timing
engine
phase
reference
averager
FPGA